

Electronic Acknowledgement Receipt

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Title of Invention:	Gate dielectric structures for integrated circuits and methods for making and using such gate dielectric structures
First Named Inventor/Applicant Name:	Maitreyee Mahajani
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Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1	Supplemental Appeal Brief	SD106US_ThirdAppealBrief.pdf	7100277 56b4935bc933e1325fccc0a50ea0b12c4aaca75	no	37

Warnings:

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